

<b>Notice of References Cited</b>	Application/Control No.	Applicant(s)/Patent Under Reexamination	
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Examiner	Art Unit	2111	Page 1 of 1
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	V	Asari, K., et al., "FeRAM Circuit Technology for System on a Chip," July 21, 1999, Proceedings of the First NASA/DoD Workshop on Evolvable Hardware, p. 193-197.
	W	"Ultrafast Nonvolatile Ferroelectric Information Storage Device," Nov. 1994, IBM Technical Disclosure Bulletin, Vol. 37, Issue 1 p. 421-424.
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Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

